

STN 8 Caplus, Japio, Inspec, USPAT FULL
STN Columbus

(FILE 'HOME' ENTERED AT 16:28:17 ON 19 AUG 2006)

FILE 'CAPLUS' ENTERED AT 16:28:26 ON 19 AUG 2006

L1 7767 S U(3A)SHAP?
L2 1418125 S ETCH? OR PATERN? OR REMOV?
L3 8600 S (HE OR HELIUM) (8A) (O2 OR OXYGEN OR O(W)SUB(W)2)
L4 18729 S TAPER?
L5 1393 S L2(10A) (L1 OR L4)
L6 4 S L3 AND L5

FILE 'JAPIO' ENTERED AT 16:29:40 ON 19 AUG 2006

L7 1 S L6

FILE 'INSPEC' ENTERED AT 16:29:57 ON 19 AUG 2006

L8 1 S L6

FILE 'USPATFULL' ENTERED AT 16:30:22 ON 19 AUG 2006

L9 171 S L3(P)L5
L10 1312147 S ANISOTROPIC? OR VERTICAL?
L11 168 S L9(10A)L10
L12 81116 S L2(10A)L10
L13 108 S L9(P)L12
L14 1679 S POSTETCH? OR POST(W)ETCH?
L15 0 S L13(P)L14
L16 0 S L9(P)L14
L17 424869 S PLASMA? OR SPUTTER? OR CORONA? OR RIE OR MRIE OR MERIE OR GLO
L18 49 S L13(P)L17
L19 74 S L13(P)POLY?
L20 18 S L19(P)L18
L21 87 S (L19 OR L18) NOT L20

FILE 'CAPLUS' ENTERED AT 16:34:19 ON 19 AUG 2006

L22 3017 S U(8A)L2
L23 6 S L3 AND L22
L24 5 S L23 NOT L6

FILE 'JAPIO' ENTERED AT 16:34:52 ON 19 AUG 2006

L25 0 S L24

FILE 'INSPEC' ENTERED AT 16:35:03 ON 19 AUG 2006

L26 2 S L24

FILE 'USPATFULL' ENTERED AT 16:35:21 ON 19 AUG 2006

L27 88979 S (U(3A)SHAP?)/CLM
L28 671642 S (ETCH? OR PATERN? OR REMOV?)/CLM
L29 81684 S (PLASMA? OR SPUTTER? OR CORONA? OR RIE OR MRIE OR MERIE OR GL
L30 112861 S TAPER?/CLM
L31 1773 S ((HE OR HELIUM) (8A) (O2 OR OXYGEN OR O(W)SUB(W)2))/CLM
L32 186 S (POSTETCH? OR POST(W)ETCH?)/CLM
L33 3853 S (L27 OR L30) (10A) L28
L34 144 S L33 AND L31
L35 50 S L34 AND L29
L36 5 S L35 NOT (L19 OR L18)
L37 40 S L34 NOT (L35 OR L19 OR L18)
SET HIGH OFF
L38 4 S L37 AND (SEMICONDUCT? OR WAFER? OR CHIP?)
SET HIGH ON
L39 4 S L37 AND L38

FILE 'CAPLUS' ENTERED AT 16:41:14 ON 19 AUG 2006

STN Columbus

L40 32 S HEO2
L41 1 S L5 AND L40

FILE 'JAPIO' ENTERED AT 16:41:56 ON 19 AUG 2006
L42 0 S L41

FILE 'INSPEC' ENTERED AT 16:42:08 ON 19 AUG 2006
L43 0 S L41

FILE 'USPATFULL' ENTERED AT 16:42:23 ON 19 AUG 2006
L44 136 S L33(P) (L31 OR L40)
SET HIGH OFF

L45 0 S L44 AND L38
L46 0 S L45 AND (SEMICONDUCT? OR WAFER? OR CHIP?)
L47 46 S L44 AND (SEMICONDUCT? OR WAFER? OR CHIP?)
SET HIGH ON

L48 46 S L44 AND L47
L49 144 S L33 AND (L31 OR L40)
SET HIGH OFF

L50 54 S L49 AND (SEMICONDUCT? OR WAFER? OR CHIP?)
SET HIGH ON

L51 54 S L49 AND L50